

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	CIRCUIT AND METHOD OF REDUCING NOISE IN AN RF POWER AMPLIFIER													
Application Number :	10/743217													
Confirmation Number:	7655													
First Named Applicant:	Alan Westwick													
Attorney Docket Number:	SIL.P0064													
Art Unit:	2817													
Examiner:	Patricia T Nguyen													
Search string:	(6771122 or 6756849 or 6828859 or 6894565 or 20040174218 or 20050068103).pn													
US Patent Documents														
Note: Applicant is not required to submit a paper copy of cited US Patent Documents														
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass							
	1	6771122	2004-08-03	Jin et al.										
	2	6756849	2004-06-29	Dupuis et al.										
	3	6828859	2004-12-07	Dupuis et al.										
	4	6894565	2005-05-17	Bocock et al.										
US Published Applications														
Note: Applicant is not required to submit a paper copy of cited US Published Applications														
init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass							
	1	20040174218	2004-09-09	Dupuis et al.										
	2	20050068103	2005-03-31	Dupuis et al.										
Signature														
Examiner Name				Date										